

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

HYUN Soo KIM, ET AL.

Application No.:

Filed:

For: **LOGIC ELEMENT EMPLOYING SATURABLE ABSORBER**

Art Group:

Examiner:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

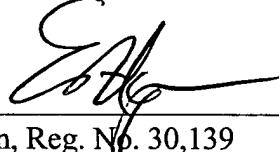
The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: 18/9/03



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PATENT
ATTORNEY DOCKET NO.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants :

Serial No.:

Filed :

Title : optical logic element

Commissioner of Patents and Trademarks
Washington DC 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

Japan Patent Application No. JP 4- 75749 is directed to an optical logic element is disclosed which performs an XOR operation through utilization of the high-speed property of light. On each branched waveguide of a Mach-Zehnder interference type optical waveguide there is provided a phase modulating element whose refractive index undergoes a change when it is irradiated by light. The interference type optical waveguide is adapted to provide different optical output levels when the refractive index changes of the phase modulating elements are both zero or a predetermined value and when they differ from each other. Thus, the optical logic element is capable of performing the XOR or XNOR operation at an ultrahigh speed.

Respectfully submitted,

Date : _____

<p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p><i>(use as many sheets as necessary)</i></p>				<p>Complete if Known</p>	
Sheet	1	of	2	Application Number	
				Filing Date	
				First Named Inventor	Hyun Soo KIM
				Art Unit	
				Examiner Name	
				Attorney Docket Number	5882P048

Examiner Signature		Date Considered	
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.

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<p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p><i>(use as many sheets as necessary)</i></p>				<p><i>Complete if Known</i></p>	
Sheet	2	of	2	Application Number	
				Filing Date	
				First Named Inventor	Hyun Soo KIM
				Art Unit	
				Examiner Name	
				Attorney Docket Number	5882P048

NON PATENT LITERATURE DOCUMENTS

Examiner Signature		Date Considered	
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